REGISTRATION FORM

Two Day National Level Workshop
On
"Embedded System Design Flow on Zynq using Vivado

18th and 19th of FEB-2016

1. Name :.................................
2. Gender :.................................
3. Designation :.................................
4. Organization :.................................
5. Qualification :.................................
6. Experience :.................................
   Teaching :.................................
   Others(specify) :.................................
7. Address for communication :.................................
   ....
   ....
8. Phone :.................................
9. Email ID :.................................
10. Bank transaction details : ....
11. Accommodation required: (Yes/No):.................................

DECLARATION

The above information given are true to the best of my knowledge. I agree to abide by the rules and regulations governing the course and shall attend the course for the entire duration. I also undertake the responsibility to inform the coordinator in case I am unable to attend the course

Place:
Date: Signature of the applicant

SPONSORSHIP CERTIFICATE

Mr/Ms/Dr_____is an
employee of our Institute / Organization and is here by sponsored, if selected.

Office Seal   Signature of sponsoring authority

Chief Patron
Er.T. P. Imbichammad, Chairman

Patron
Prof. P. O. J. Lebba, Secretary

Advisory Committee
Dr.V. H. Abdulsalam, Principal
Dr. K. P. Mohammed, Director
Dr. K. P. Mohandas, Dean MESCE
Dr. Lillykutty Jacob, Chair IEEE MSS
Dr. Badeeuzzaman, Vice Principal, MESCE

Convener
Dr. A Gopakumar, HOD, ECE

Co-ordinators
Mr. Rejiram R, Asst. Professor, ECE
Mrs. Sreediya R, Asst. Professor, ECE

For More Details...

MES College of Engineering, Kuttippuram,
Thrikkanapuram PO,Malappuram (Dist),
Kerala - 679573

04943051210, 04943051212
9895364127, 9746998333, 9495124360

www.mesce.ac.in

gopakumar@mesce.ac.in
cecece@mesce.ac.in
rejirammes@gmail.com
diyasreediya@gmail.com

Two Day National Workshop
on
"Embedded System Design Flow on Zynq using Vivado"

18th & 19th of February 2016

Organized by

MES COLLEGE OF ENGINEERING KUTTIPPURAM

Department of Electronics and Communication Engineering

in association with

IEEE Malabar Subsection
&
CoreEL Technologies (I) Pvt. Ltd. Bangalore

MES Engineering College

M.E.S College of Engineering Kuttippuram the first private Engineering College established under the self financing sector in Kerala is situated on the beautiful and serene banks of Bharathapuzha. It was established as an institution with minority status in 1994. Widely acknowledged as one of the premier institutions imparting technical education in the state, the college offers admission to all categories of students with special consideration to educationally backward communities. At present college offers eight undergraduate programmes including B Arch and ten post graduate courses in M Tech, MCA, MBA and M Arch. Nearly 3000 students study here under the guidance of over 240 qualified faculty members and research guides.

The ECE Department

The department started functioning right from the inception of the college in 1994. Currently Dept offers B Tech Electronics and Communication Engineering consisting of 2 batches and a post graduate program in Communication Engineering and Signal Processing, started in the year 2012. The department is accredited twice by NBA in the years 2004 and 2009. Both the B-Tech and M-Tech Degree offered by the Department follows the curriculum and syllabi as well as the rules and regulations of the University of Calicut and APJ Abdul Kalam Technological university. The department has nine well equipped state of the art laboratories.

About CoreEL Technologies

CoreEL University Technologies is a technology company with business spread across design services & product development, distribution and training. Head Quartered in Bangalore, India, CoreEL is a leading provider of VLSI & Embedded Systems design services and Intellectual Property. Since its inception in 1999, CoreEL Technologies, a privately held corporation has always strived to deliver quality solutions & support in all the business areas that it serves. CoreEL Technologies are the Authorized Training Partner (ATP) of Xilinx in India and Xilinx University Program (XUP).

About the Workshop

This course provides an introduction to embedded system design flow on Zynq using ZedBoard and Xilinx Vivado* design software suite.

Highlights

After the completion of this training program the participants will be able to:

- Describe the general Artix-7 All Programmable FPGA architecture
- Understand the Vivado design flow
- Create and debug HDL designs
- Configure FPGA and verify hardware operation
- Rapidly architect an embedded system targeting the ARM processor of Zynq located on ZedBoard using Vivado and IP Integrator
- Extend the hardware system with Xilinx provided peripherals

Course Contents

- Basics of FPGA & Vivado
- 7-Series Architecture Overview
- Zynq architecture overview
- HDL coding & Synthesis Technique
- Implementation and Static Timing Analysis
- Lab 1: Vivado Design Flow
- Lab 2: Synthesizing a RTL Design
- Lab 3: Implementing the Design
- Introduction to Embedded System Design using Zynq
- Extending the Embedded System into Programmable Logic
- Adding Your Own IP Peripheral
- Software Development Environment
- Software Development and Debugging
- Lab 4: Simple Hardware Design
- Lab 5: Adding Peripherals in Programmable Logic
- Lab 6: Creating and Adding Your Own Custom IP

Resource Person

Mr. Bikash R P
Application Engineer,
University Program Division.
CoreEL Technologies (I) Pvt. Ltd.
Bangalore-560034, India

Important Dates

Last date of receipt of Application : 12/02/2016
Intimation about selection : 15/02/2016
Confirmation from the participants : 16/02/2016

How to Apply

Filled in application form along with transaction details may be sent by post / Email to :

Dr. A Gopakumar
Professor and Head
Dept. of Electronics & commuication Engineering
MES College of Engineering, Kuttippuram,
Thrikkanapuram PO, Malappuram District,
Kerala - 679573
Ph: 0494 – 3051210, Fax : 0494 – 2698081
Email: gopakumar@mesce.ac.in
        rejirammes@gmail.com

Registration fee for the course can be paid through Net banking/ NEFT /DD (Please specify the Applicant's name)

Account Name: Continuing Education Cell Dept of ECE MESCE
Bank Name: Punjab National Bank
Branch Name: Thrikkanapuram
Bank Account No: 6232000100004233
IFSC Code: PUNB0623200

Registration Fee

Participants (IEEE Member) - Rs 500/-

Non-IEEE Member- Rs 700/-